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PAM, PWM, PPM.

Reference/Suggested Books

- 1. Taub and Schilling: Principles of Communication Systems 3/e, TMH**
- 2. B P Lathi : Modern Analog & Digital Communication System, Oxford University Press**
- 3. Simon Hykin: Communication Systems, John Wiley and Sons**
- 4. R P Singh and S D Sapre: Communication System Analog & Digital 2/e. (TMH)**
- 5. G. Kennedy and B. Davis , “Electronic Communication Systems” Tata McGraw Hill**
- 6. Roy Blake, “ Wireless Communication Technology” Thomson Asia Pvt. Ltd. Singapore**

5EE6.3 INTRODUCTION TO VLSI

Unit-1 Introduction to MOS Technology: Basic MOS transistors, Enhancement Mode transistor

action, Depletion Mode transistor action, NMOS and CMOS fabrication.

Unit-2 Basic Electrical Properties of MOS Circuits: I_{ds} versus V_{ds} relationship, Aspects of threshold

voltage, Transistor Transconductance gm. The nMOS inverter, Pull up to Pull-down ratio for a

NMOS Inverter and CMOS Inverter (B_n/B_p), MOS transistor circuit Model, Noise Margin.

Unit-3 CMOS Logic Circuits: The inverter, Combinational Logic, NAND Gate NOR gate, Compound

Gates, 2 input CMOS Multiplexer, Memory latches and registers, Transmission Gate, Gate

delays, CMOS-Gate Transistor sizing, Power dissipation.

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